



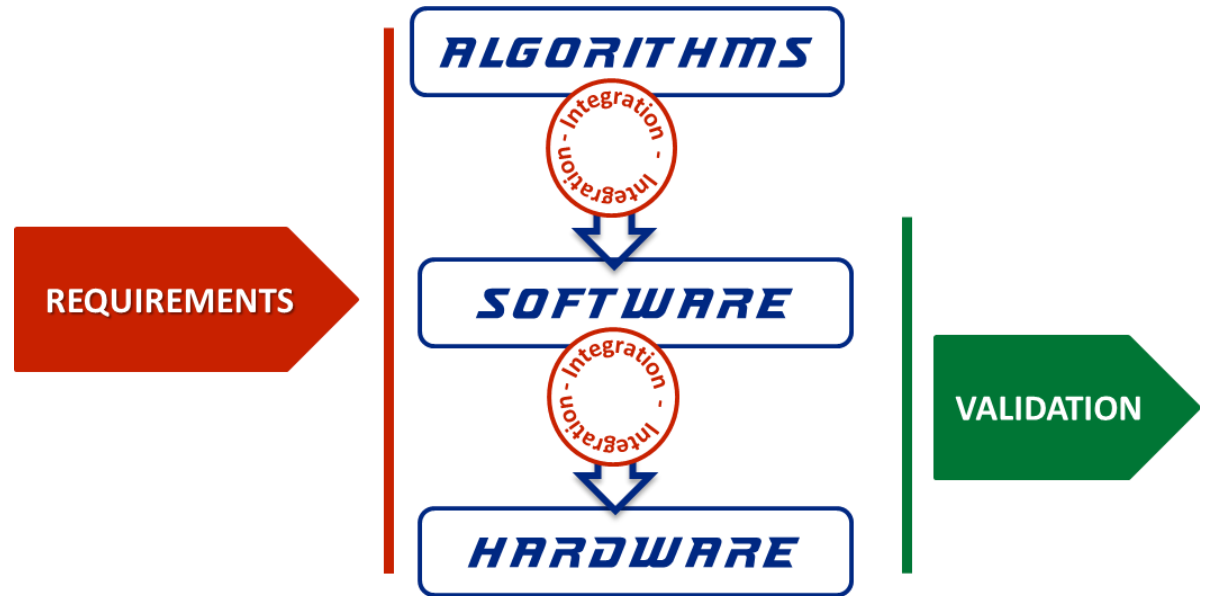
*SMART AND SCALABLE SATELLITE HIGH SPEED PROCESSING CHAIN*

---

# S4Pro Project Overview

# INTRODUCTION

- S4Pro stands for: Smart and Scalable Satellite high Speed Processing Chain
  - Reference missions and algorithms optimisation
  - Processing System software and performance benchmarks
  - Hardware system and integration
  - Validation up to TRL6



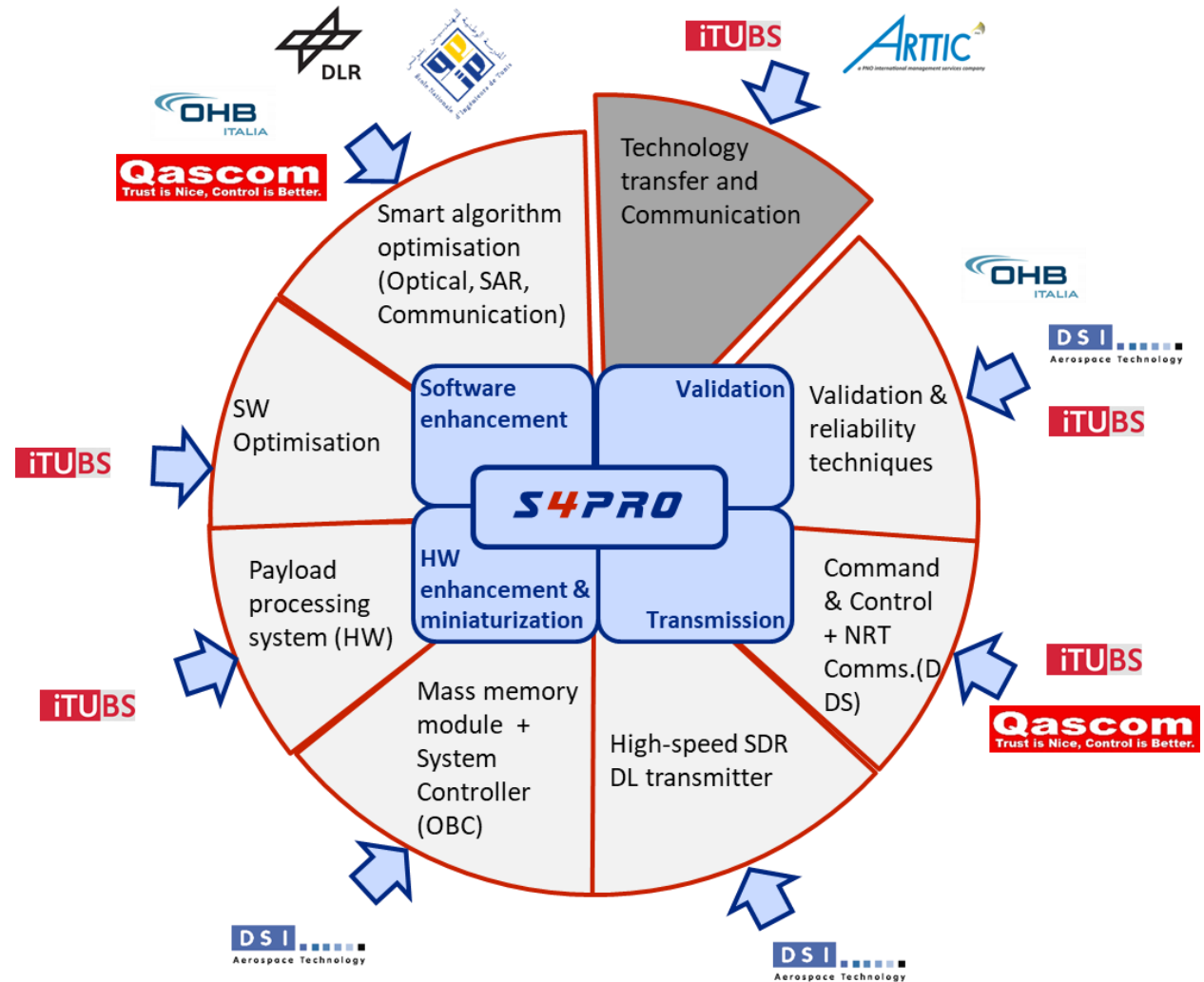
## KEY FACTS

- ✓ START DATE: 1st November 2018
- ✓ DURATION: 36 months
- ✓ BUDGET: 2.654.391 €

# S4PRO PARTNERS

- The S4Pro Consortium is comprised of 7 partners representing 3 European and 1 Associated (Tunisia) countries:

- 3 SMEs: iTUBS, DSI, QASCOM
- 1 industrial partner: OHB Italia
- 1 Research Centre: DLR
- 1 University: ENIT
- 1 Innovation Management Consultancy: ARTTIC



# PROJECT OBJECTIVES

S4Pro Objectives fit into two larger categories:

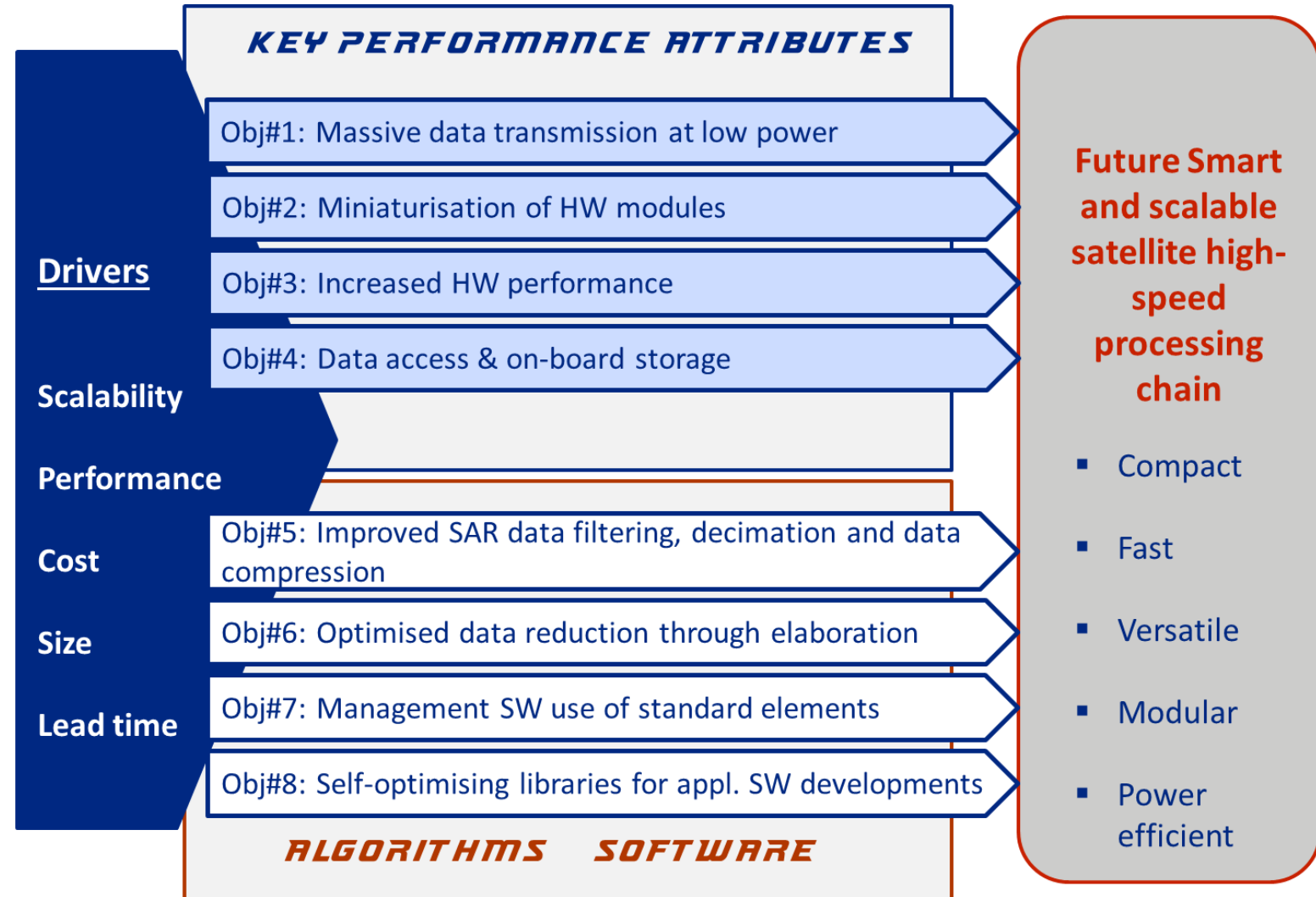
HW aspects

Objectives 1-4

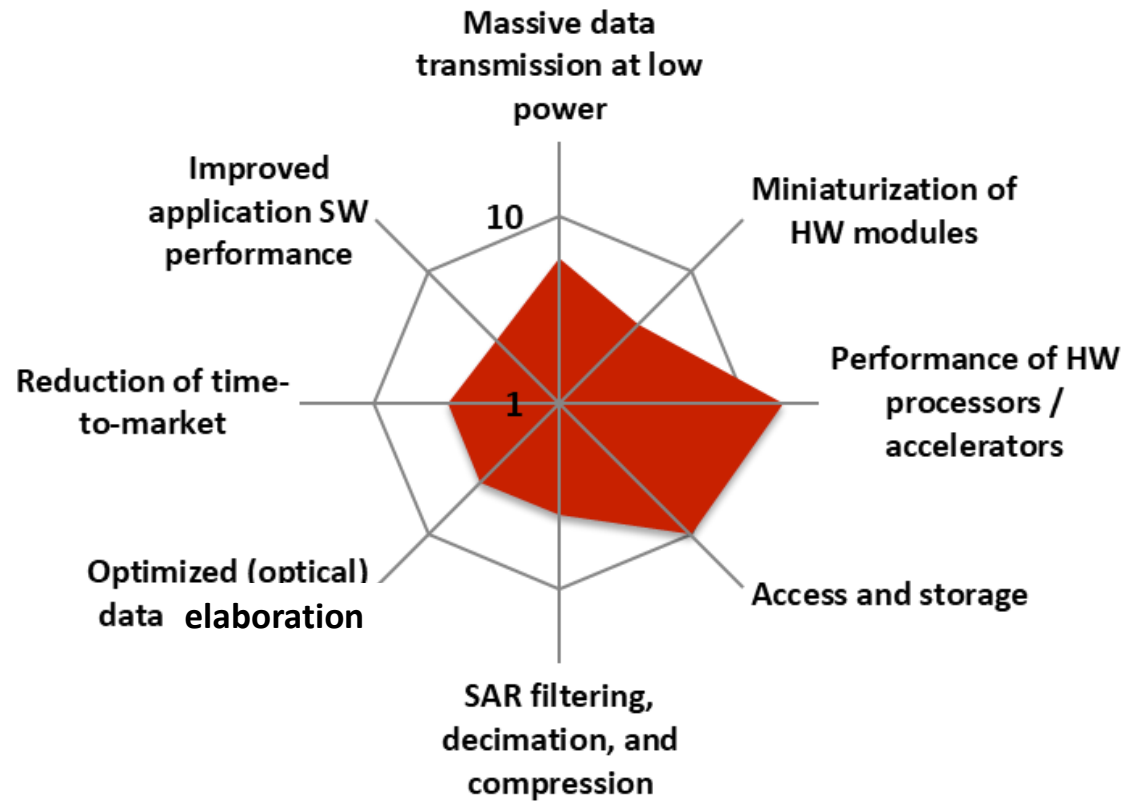
Mission related

algorithms and system  
software

Objectives 5-8



# PROJECT OBJECTIVES



■ SOTA  
■ PBSOTA

S4Pro Objectives / Factor of improvement	SOTA*	PBSOTA**
Massive data transmission at low power	1	6
Miniaturization of HW modules	1	4
Performance of HW processors / accelerators	1	16
Access and storage	1	10
SAR filtering, decimation, and compression	1	4
Optimized (optical) data elaboration	1	4
Reduction of time-to-market	1	4
Improved application SW performance	1	3

\*SOTA – State-of-the-art

\*\*PBSOTA – Progress beyond the State-of-the-art

# INNOVATION

- The following products and services will emerge from the S4Pro Results:



## **HIGH SPEED DATA CHAIN**

providing compact high-end payload data processing and high data rate wireless communication for small EO satellites



## **COMPLEX ALGORITHMS**

making it possible to compress data before transmission, reducing downloading time and saving memory space



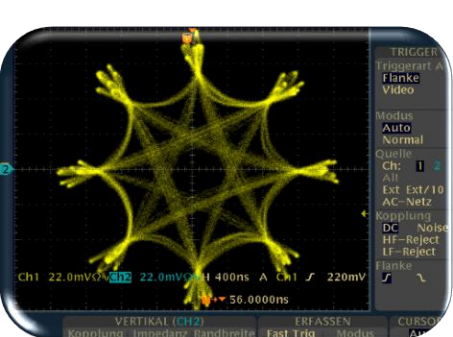
## **EMBEDDED SW LIBRARY**

Enabling signal & image processing routines for present and future mission scenarios



## **OPTIMIZED HW SW**

enabling data compression, data-chain transmission and improved memory storage



## **GENERIC HIGH PERFORMANCE SDR**

that is interoperable with RF or optical front-ends



## **HW SW MODULES**

as enabler for high bandwidth transmissions for nano- and small satellites a key enabling element for the next generation of EO satellites.



## **SOFTWARE TOOLCHAIN**

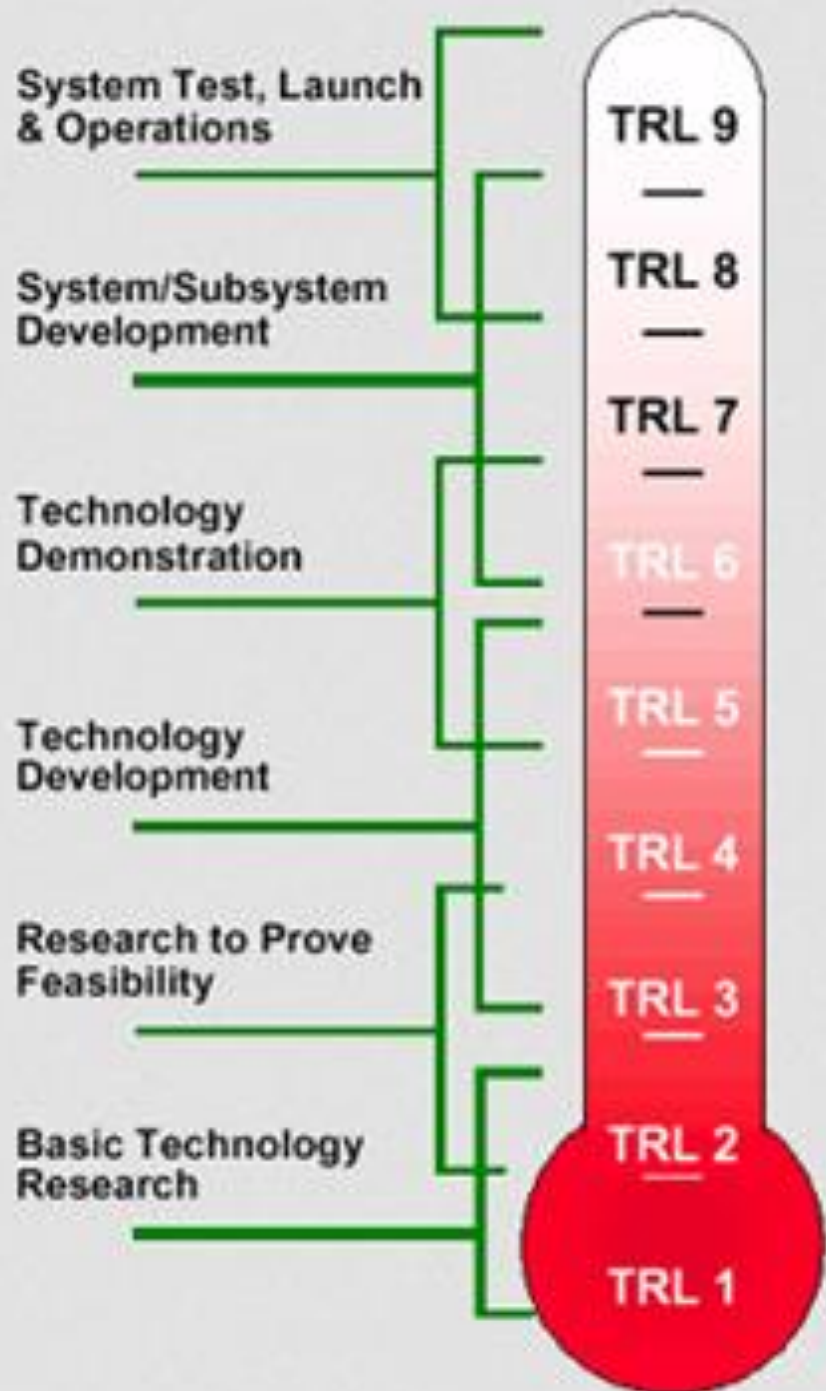
to support multiple advancing communication standards (CCSDS and DDS)



## **BUSINESS AND ORGANISATIONAL MODELS**

Knowledge transfer, including associated countries and establishment of new partnerships and exchanges.

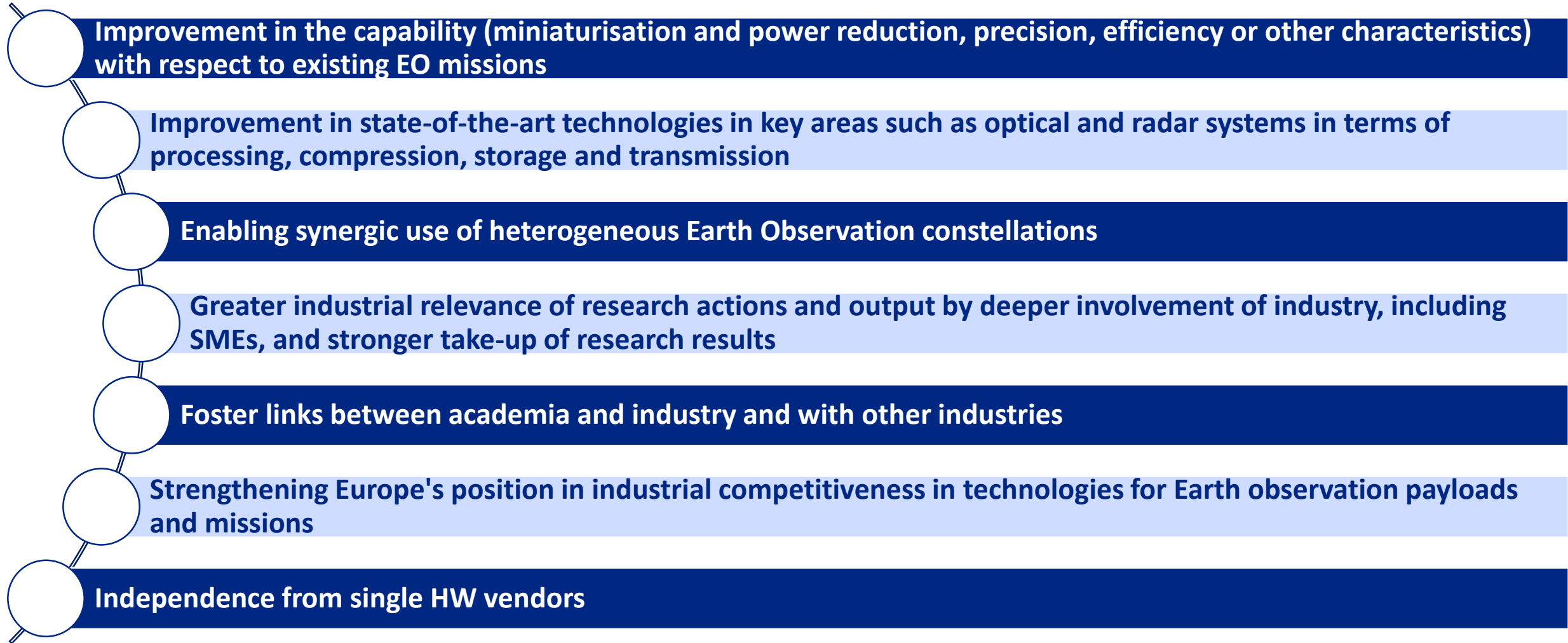




# TRL LEVEL

Technology	TRL at S4Pro start	Target TRL
High-performance Scientific and Signal Processing Software Libraries	TRL3	TRL6
Management SW		
Communication and navigation SW		
Mass memory	TRL3+	
Payload processing unit	TRL3	
High-Performance Multi-Core Processor		
Optimised algorithms for on-board SAR pre-processing	TRL3+	
Optimised and application algorithms for Optical	TRL3	
Wide-band modem for small satellites	TRL4	
Wide-band SDR		
TT&C + GNSS module		

# EXPECTED IMPACT





# THANK YOU

## ANY QUESTIONS?



This project has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 822014. This text reflects only the author's views and the Commission is not liable for any use that may be made of the information contained therein.

